M.Sc.-I (Computer Science) (CBCS Pattern) Semester - II

PSCSCT05 - Paper-I: Theory of Computation & System Programming

P. Pages: 2 Time: Three Hours			ours	1818 1888 1818 1	UG/S/23/11187 Max. Marks : 80	
	Notes	s: 1 2 3		All the questions are compulsory and carry equal marks. Draw neat and labelled diagram wherever necessary. Avoid vague answers and write answers relevant and specific to question.	ons only.	
		Eith	er:			
1.	a)	Expl	ain]	NDFA with E-moves and 2DFA.	8	}
	b)	Prov	e fo	llowing language as not Regular $L = \{0^{n,n} \mid n \ge 1\}$	8	}
				OR		
	c)	Wha	t is 1	finite state machine? Explain in detail.	8	}
	d)	Cons i) ii)	Cor	et DFA which accepts languages of all string over the alphabet $\Sigma = (a, b)$ ntain ab as substring. In tains two consecutive zeros any where in the string 0m $\Sigma = \{0, 1\}$	9) 8	;
		Eith	er:			
2.	a)	Expl	ain (Chomsky Hierarchy with the help of diagram.	8	}
	b)	-		and define PDA with diagram and Design PDA for language accepting), $L = \left\{ a^n \ b^n \ \ n \geq 1 \right\}$	string over 8	}
				OR		
	c)	Desc	cribe	the Turing Machine as enumeration.	8	}
	d)	and $\delta(q_0)$	δ is	rammar for the language N(M) where $M = \{q_0, q_1\}, \{0,1\}, \{x, z_0\}, \delta, q_0\}$ given by : $z_0 = \{(q_0, xz_0)\}, \delta(q_1, 1, x) = \{(q_1, \epsilon)\}$	$\{0, z_0, \phi\}$;
				$\mathbf{x} = \left\{ \left(\mathbf{q}_{0}, \mathbf{x} \mathbf{x} \right) \right\}, \ \delta \left(\mathbf{q}_{1}, \in, \mathbf{x} \right) = \left\{ \left(\mathbf{q}_{1}, \in \right) \right\}$		
		o(d)	յ, I, Ջ	$(x) = \{(q_{2,} \in)\}, \delta(q_{1}, \in, z_{0}) = \{(q_{1}, \in)\}$		

Either:

3. a) What are security issues in Device Drivers.

8

b)	Explain phases of compiler in detail.	8		
	OR			
c)	Explain the various class of devices and modules.	8		
d)	Differentiate between Kernel Module and application.	8		
	Either:			
a)	Explain the concepts of Loading and Linking in detail.	8		
b)	What are different addressing modes?	8		
	OR			
c)	What are interrupts? Explain Interrupt service Routines.			
d)	Explain Instruction set and formats of 8086 microprocessor family.			
	Attempt all the questions.			
	a) Explain Application of Finite state Automata.	4		
	b) What is context sensitive language.	4		
	c) Describe Kernal symbol table in detail.	4		
	d) Explain Role of Assemblers and Macros in detail.	4		
	c)d)a)b)c)	C) Explain the various class of devices and modules. d) Differentiate between Kernel Module and application. Either: a) Explain the concepts of Loading and Linking in detail. b) What are different addressing modes? OR c) What are interrupts? Explain Interrupt service Routines. d) Explain Instruction set and formats of 8086 microprocessor family. Attempt all the questions. a) Explain Application of Finite state Automata. b) What is context sensitive language. c) Describe Kernal symbol table in detail.		
