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CONVENER

Dr. A Shirly Edward

HOD - ECE

EVENT CO-ORDINATORS

Dr. S Karthik - ECE

Mr. A. Dinesh Babu - ECE

ABOUT SRM IST

Education is all about creating an environment of academic freedom, where bright minds meet, discover and learn. One would experience top of the world living and learning experience at SRM, SRM Institute of Science and Technology (formerly known as SRM University) - Where you have the freedom to take wings. SRM Institute of Science and Technology is one of the top ranking universities in India with over 52,000 full time students and more than 3200 faculty across all the campuses Kattankulathur, Ramapuram, Vadapalani Campus all in and around Chennai, and Modinagar, NCR, Delhi offering a wide range of undergraduate, postgraduate and doctoral programs in six Faculties - Engineering & Technology, Management, Medicine & Health Science, Science & Humanities, Law and Agricultural Science.

ABOUT THE FDP

The FDP prepares participants to design, debug, and implement efficient, optimized applications using the LabVIEW FPGA Modules and reconfigurable I/O hardware. Participants will understand how to compile and deploy their VIs to different types of NI targets such as CompactRIO, Controllers and Modules, myRIO etc. Participants will learn how to develop applications, and will learn to acquire digital and analog I/O, synchronize operations, implement signal processing and pass data between their host VI and their FPGA target.

TOPICS

- > Introduction to LabVIEW for FPGA programming
- > Hands-on LabVIEW Environment, Data types and Structures
- > Introduction to NI myRIO
- > Hands-on programming using NI myRIO
- > Hands-on LabVIEW FPGA Concepts
- > Knowing Power and Limitations
- > Using FPGA I/O
- > Hands-on Modular FPGA code
- > Hands-on FPGA Signal Processing

RESOURCE PERSON

Senior Application Engineers From
VI SOLUTIONS, BANGALORE



Register Here



or

Click here to register



SRM INSTITUTE OF SCIENCE AND TECHNOLOGY
Vadapalani, Chennai - 600 026, Tamil Nadu, India
College of Engineering and Technology
Department of Electronics and Communication Engineering
Presents

FACULTY DEVELOPMENT PROGRAM

ON

D³

(DESIGN · DEVELOP · DECODE)

GUI BASED FPGA PROGRAMMING

Date :
20TH DECEMBER 2021
TO
24TH DECEMBER 2021

- > Free Registration
- > Participants will be selected in FCFS basis.
- > E-Certificates will be provided to the participants.

CONTACT
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Zoom Meeting ID : to be shared in the Whatsapp Group